



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Patent Application of

Yuri Mirgorodski et al.

Application No.: 10/664,469

Filed: September 17, 2003

For: A METHOD OF PMOS STACKED-  
GATE MEMORY CELL  
PROGRAMMING ENHANCEMENT  
UTILIZING STAIR-LIKE PULSES OF  
CONTROL GATE VOLTAGE

Group Art Unit: Unknown

Examiner: Unknown

**PRELIMINARY AMENDMENT**

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Commissioner for Patents  
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STALLMAN & POLLOCK LLP

Dated: 01/12/04

By: Janet Chan

Janet Chan

Sir:

Please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing of claims that begins on page 2 of  
this paper.

**Remarks** begins on page 4 of this paper.